



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Udupa, et al.

Art Unit: 2816

Serial No.: 10/724,517

Examiner: Zweizig, Jeffrey S.

Filing Date: 11/29/2003

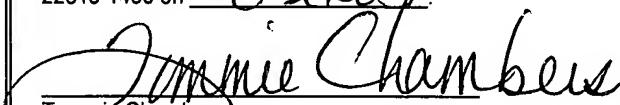
Docket No.: TI-37240

Title: ENHANCING SNR AND THROUGHPUT PERFORMANCE OF INTEGRATED CIRCUITS

LETTER TO THE OFFICIAL DRAFTSPERSON

Assistant Commissioner for Patents
ATTN: Official Draftsperson
Washington, D.C. 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)
I hereby certify that the above correspondence is being deposited with
the U.S. Postal Service as First Class Mail in an envelope addressed to:
Assistant Commissioner for Patents, PO Box 1450, Alexandria, VA
22313-1450 on 8-29-05.

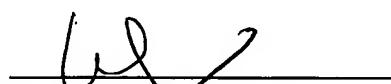

Tommie Chambers

Dear Sir:

Applicants propose to add Figure 5C as noted on the enclosed drawing.

Charge any necessary fee to Deposit Account No. 20-0668. This authorization is enclosed in triplicate.

Respectfully submitted,


W. Daniel Swayze, Jr.
Reg. No. 34,478
Attorney for Applicants

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TI-37240